

Company Overview



January 2015

- Leading Edge is a privately owned company founded in 2005 by Pietro Vergine and Nigel Woolaway
 - Both of the founders have extensive experience in the EDA arena
- Leading Edge is based in the Bergamo area enabling easy access to the major sites of electronic design in Europe

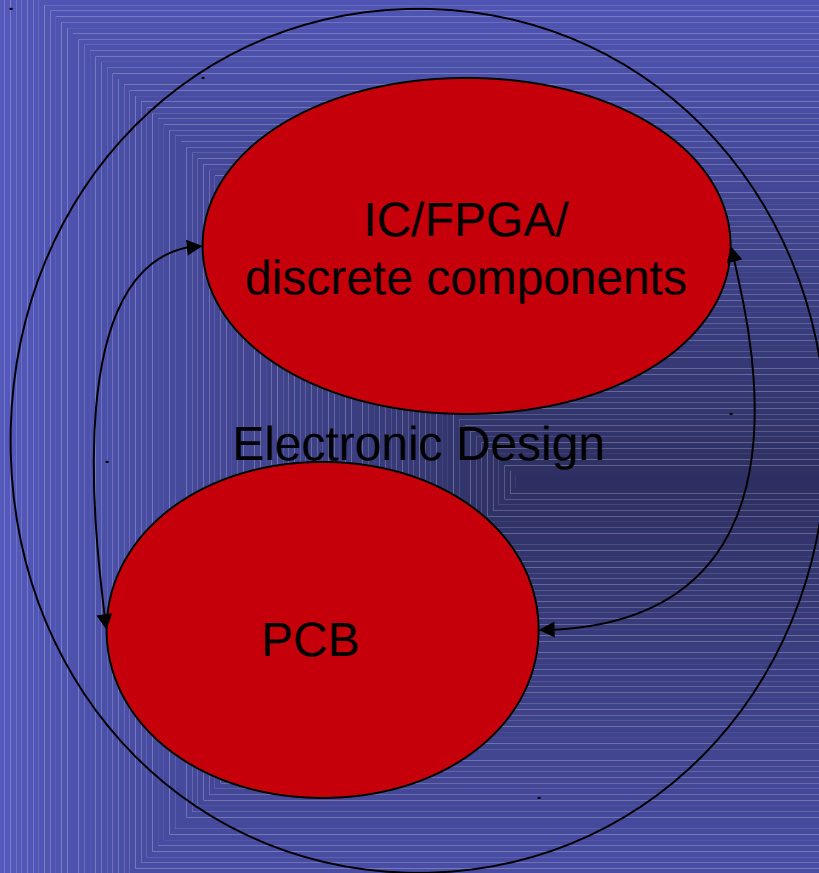
- Deploy innovative EDA tools in the design community thus assisting the growth of innovative EDA tool providers
- Improve the productivity of design teams
 - Providing training on the effective use of EDA tools
 - Providing methodology consulting
- Provide solutions to customer needs
 - Resources, competencies, know-how, HW/SW design

- Local representation for marketing, sales and technical support
- Identification of potential users
- Local front-line technical support during tool evaluation
- Post-sales technical support
- Customised training services

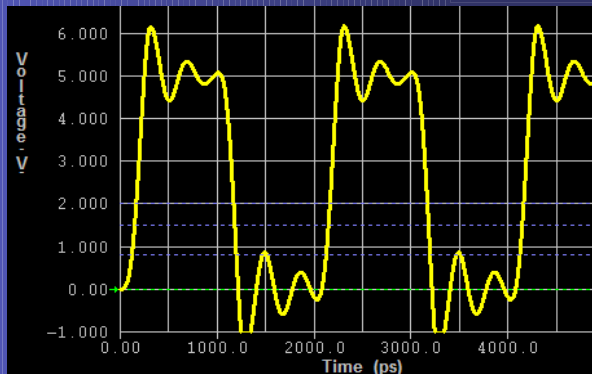
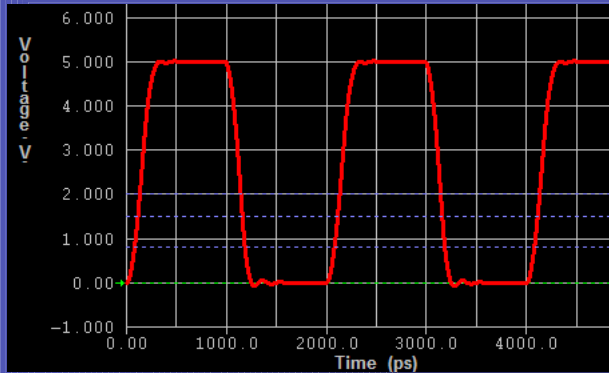
- Training and methodology consulting to improve efficiency
- Fast identification of innovative solutions for your design problem
- Qualification of tools for use in your design environment
- Integration of new tools into an established design flow
- Design and verification service

- Leading Edge is a training partner for Mentor Graphics, Altera, Willamette and IPC teaching their classes in Europe and the Middle East
- Leading Edge has internally developed classes on:
 - TCL/TK programming language
 - Verilog/VHDL coding style
 - Design methodologies for high speed PCB

Design Domain Interactions



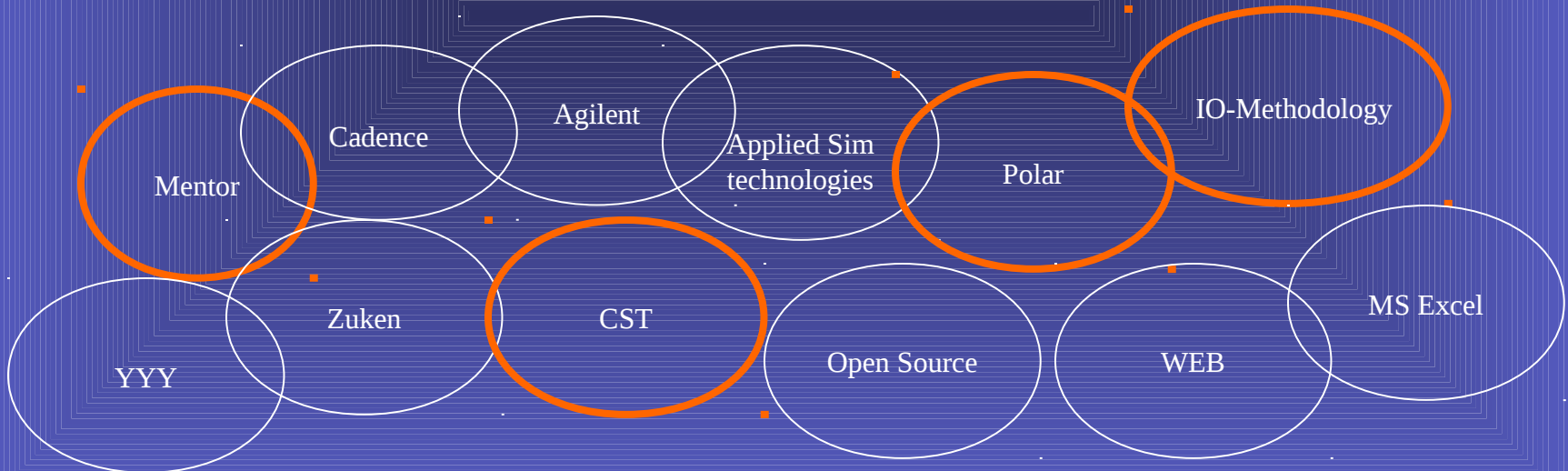
- Different design domains have both shared and distinct requirements
- Methodology, tools and competences are key aspects to consider during a design process and finding a good compromise is not an easy task
- Leading Edge can help design teams in finding solutions to their needs helping with the integration in current design environment



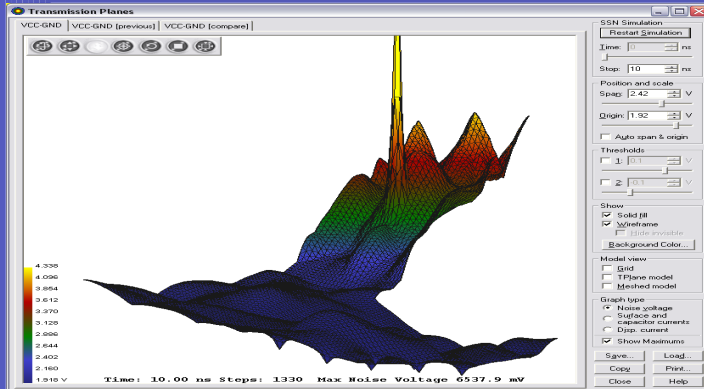
- As design complexity and frequencies increase, signal integrity becomes a major source of design failures
- Reflection noise, crosstalk, ground bounce, transmission losses, voltage drop, rail collapse, power supply noise and EMI all play a role in system robustness

SI and EMC Analysis Tools

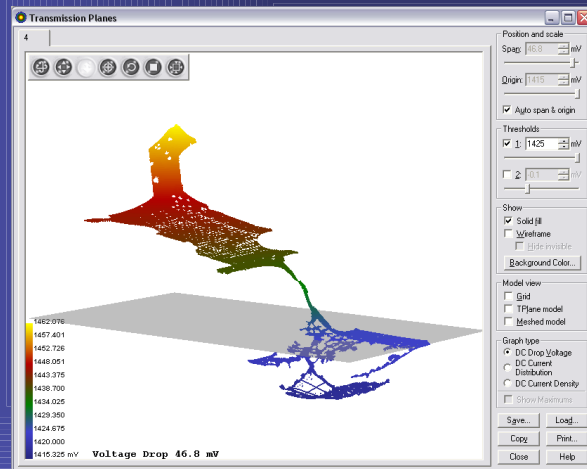
- Several tools can be used to analyze SI/EMC
 - All depends on trade-off between accuracy, speed and easy-of-use
- Some examples from the CAD/EDA tools domain
 - More examples and use cases in our SI courses



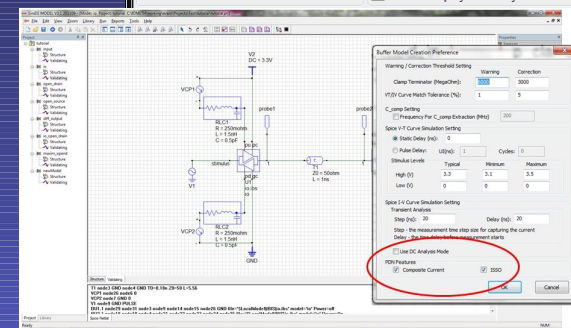
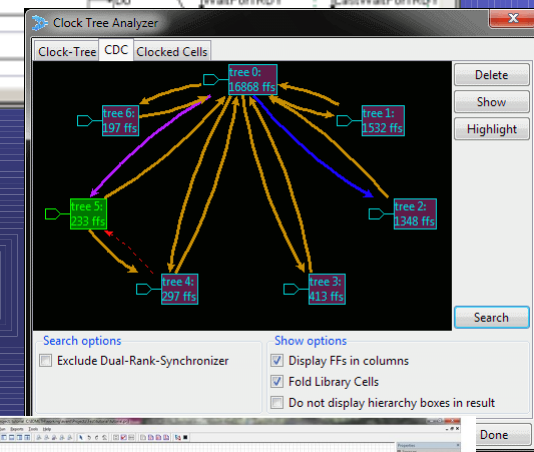
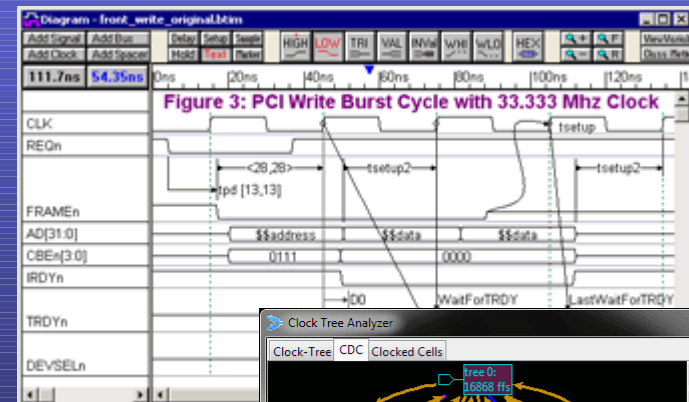
Signal Integrity Analysis Services



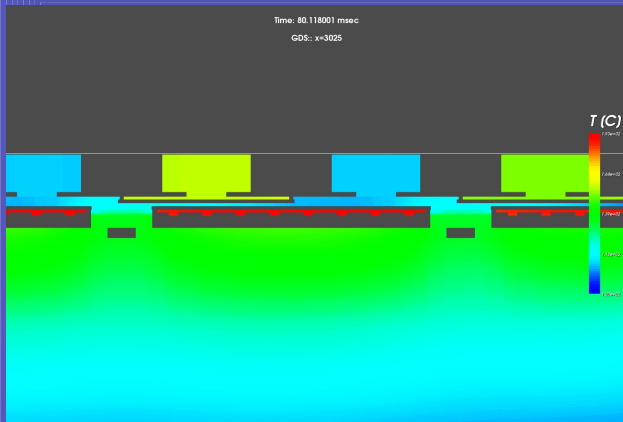
- In addition to courses on high-speed PCB design, Leading Edge offers signal integrity analysis services using state of the art software tools



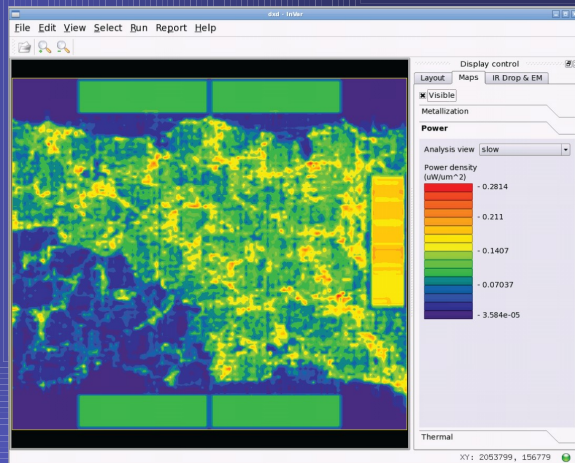
- Synapticad
 - Testbench generation tools
 - (www.syncad.com)
- Concept Engineering GmbH
 - Visualisation tools
 - (www.concept.de)
- Orora Design Technology
 - Analogue model abstraction
 - (www.orora.com)
- IOMethodology
 - Automated IBIS model generation
 - (www.iometh.com)



EDA Partners – Power Analysis



- Magwel
 - Power transistor electro-thermal analysis tools
 - (www.magwel.com)



- Invarian
 - SoC level electro-thermal analysis tools
 - (www.invarian.com)

- Ngenics
 - Cell library optimisation tools
 - (www.ngenics.com)
- Ellexus
 - Debugging tools for software installation and deployment
 - (www.ellexus.com)
- Redhat
 - OS Provider
 - (www.redhat.com)

Design Service Partners

- Board Level
 - RSM, Gest Labs
- IC Level
 - HDL Design House (www.hdl-dh.com)
- Certification
 - VCA, DFB, 4SGroup

- Leading Edge is a licensed IPC training center for CID (Certified Interconnect Designer) class and, in collaboration with Gest Labs, can also offer other IPC certifications

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