



Teklatch bridges the analog/digital design gap

Start-up launches design tool for mixed-signal and EMI/EMC sensitive ASIC design @ DATE 2009 conference 21-23 April in Nice, France

Nice, France, April 22nd 2009... Start-up EDA company Teklatch is releasing a break-through design automation technology at this year's DATE, a technology which successfully bridges the gap between analog and digital design optimization. The technology represents a major step forward for e.g. wireless design, which is suffering under the disturbance of noise from larger digital circuitry and faster current transients. Other high impact segments include noise sensitive digital ICs in highly integrated systems such as mobile phones, as well as automotive and aerospace chips with strict EMI/EMC compliancy requirements. With semiconductor technologies in nano scale geometries, power and noise integrity presents a major obstacle to design closure, inhibiting the optimal utilization of the newest and most advanced fabrication technologies.

Currently a host of analysis tools exist to assist IC designers in the sign-off phase, but until now implementation-phase optimization has been out of reach. Teklatch's FloorDirector™ technology now makes it possible to optimize the dynamic content of digital ASIC power grid currents in an automated flow and immediately view results in the frequency domain. This makes a range of optimization targets accessible to designers.

“The break-through innovations embedded in FloorDirector 2.0 allow true mixed-signal design optimization. The bridge enabled between the analog and digital domains is unprecedented. The fact that the technology is applicable in the implementation-phase allows designers to not merely analyse and iterate, but to actually take proactive steps for noise convergence. Our clients are currently harvesting these benefits”, says founder and CEO, Dr. Tobias Bjerregaard.

Nigel Woolaway, President of Leading Edge Snc comments: “Teklatch's FloorDirector technology effectively addresses important challenges regarding EMI/EMC compliance, challenges to which the industry currently provides no satisfactory solution. This ground breaking technology solves pressing design challenges, and we expect high impact in a range of applications”.

With the newest release of the FloorDirector EDA software, released at DATE 2009 in Nice, France, Teklatch makes these capabilities readily available to the IC design community. The tool instantiates an analysis and automated optimization framework.

Visitors to booth B31 at DATE will be able to see a demo of the FloorDirector tool, used in the design of an EMC sensitive, automotive multiprocessor design.

About Teklatech

A technology visionary and industry pioneer, Teklatech provides targeted electronic design automation (EDA) solutions to the semiconductor industry. With innovations in noise and power integrity, floorplanning and clock distribution networks, Teklatech is focused on meeting the stringent demands of next-generation semiconductor products. Teklatech works closely with industry leaders to ensure the compatibility of tools and design flows in order to serve customers with full solutions.

www.teklatech.com

About Leading Edge Snc

Based in Milan, Leading Edge provides European-wide services to the EDA industry and Electronic Design community. As well as being the leading provider of local technical support services to start-ups targeting the European marketplace, Leading Edge provides design flow consulting, tool qualification and tool integration services to design teams. Leading Edge is a certified Mentor Graphics training partner and Altera Technical Training Partner for Italy.

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